



## Power Amplifier Linearization Using Digital Pre-Distortion Techniques Based On Memory Polynomial Model and Estimated By Self Organizing Migrating Algorithm

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**ABSTRACT:** To gratify the requirements of modern wireless communication, linearization of power amplifier (PA) becomes a foremost research field. Several linearization techniques have been introduced time to time. Digital pre-distortion linearization technique is the best linearization technique from other linearization techniques. In this paper, modified memory polynomial function is used to model PA and digital pre-distorter. Self organizing migrating algorithm (SOMA) is applied to estimate the coefficients of the PA and digital pre-distorter models. The coefficients of PA and DPD are optimized iteratively in order to minimize the output PSD around the pre specified frequency. The simulation results for Worldwide Interoperability of Microwave Access (WiMAX) 10 MHz PA system shows the extraction solution achieves excellent linearization accuracy. The adjacent channel leakage ratio (ACLR), error vector magnitude (EVM) and number coefficients of the PA and digital predistorter are calculated of the proposed memory polynomial model. Improvement in ACLR (dB) and EVM (dB) shows the correctness of the proposed technique.

**Keywords:** Digital predistorter, memory effects, memory polynomial, non-linearity, power amplifier, self organizing migrating algorithm and WiMAX

### I. INTRODUCTION

PA is a key component of the communication system and its approach to achieve the highest possible power efficiency under linearity requirements [1,5]. In order to improve the PA's efficiency and nonlinearity, some highly efficient linearization techniques are used. The classification of linearization techniques is uncertain but these techniques can be classified into the following categories [2-4]. Boot up bias linearization technique, dynamic bias linearization technique, feedback linearization techniques (baseband envelope feedback linearization technique, polar loop modulation feedback linearization technique, cartesian loop modulation feedback linearization technique), envelope elimination and restoration linearization technique, feedforward linearization technique, linear amplification using non linear components (LINC) linearization technique and digital Pre-distortion (DPD) [5,12-14]. Among all of these existing linearization techniques, DPD linearization techniques has become the most popular linearization technique for its good performance. Digital pre-distorter can be modeled by look up table (LUT), Volterra series and memory polynomial. LUT is based on stored PA data in tabular form so it increases the complexity. To compensate for the strong nonlinearity, higher memory polynomial order, longer memory depth and more samplings are required [15-17]. Volterra series suffered from the problem, the number of model coefficients rapidly increase with the increase of polynomial order and memory depth. Memory polynomial model is simplified version of Volterra series model. Hence modified memory polynomial model is used in proposed work. In this work, the achievability of PA memory polynomial behavioral models is analyzed by using SOMA. SOMA estimates the model coefficients from the view of probability [5,18-20]. The performance of the proposed method is verified by simulations for WiMAX system. This paper is organized as follows; First section introduction, in section II memory polynomial function, section III introduce the SOMA, simulation results are in section IV and the conclusion is presented in last section.

### Memory Polynomial Function

The memory polynomial expression implemented in the proposed work is given for the output is given as [3,5,11];

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$$y_{MP}(n) = \sum_{k=0}^{K-1} \sum_{l=0}^{L-1} c_{l,k} x(n-l) |x(n-l)|^k \quad (1)$$

where the signals  $x(n)$  and  $y_{MP}(n)$  are the complex baseband input and output waveforms, respectively, and  $c_{l,k}$  represents the model's coefficients.  $L$  represents memory length and  $K$  is the order of non-linearity. The output of a PA and DPD modeled using memory polynomial model can be written in vector form as [5,18]

$$y_{MP}(n) = \mathbf{u}(n)\mathbf{c} \quad (2)$$

Estimation of the coefficients  $c_{l,k}$  can be carried out by considering the solution of equation 5.1 as solution of linear equation;

$$\mathbf{A}\mathbf{x} = \mathbf{B} \quad (3)$$

The coefficients of PA and DPD model are estimated by SOMA

### Self Organizing Migrating Algorithm

SOMA belongs to a member of the family of swarm intelligence algorithms which mimics the competitive-cooperative behaviour of a pack of intelligent agents. This algorithm is based on effective combination of exploration and exploitation. During last two decades, SOMA has been used by numerous researchers for solving diverse tasks [7-9]. SOMA has very fast convergence rate, it based on a population of individuals in loops called migration loops. In each migration loop, the population is evaluated, and the solution with the highest fitness becomes the chef. Apart from the chef, in one migration loop, all individuals will traverse the input space in the direction of the chef. An individual will travel a certain path length towards the chef in  $n$  steps of defined length. If the *path length*  $> 1$ , then the individual will overshoot the chef. Flow chart of SOMA process is given in Figure 1.

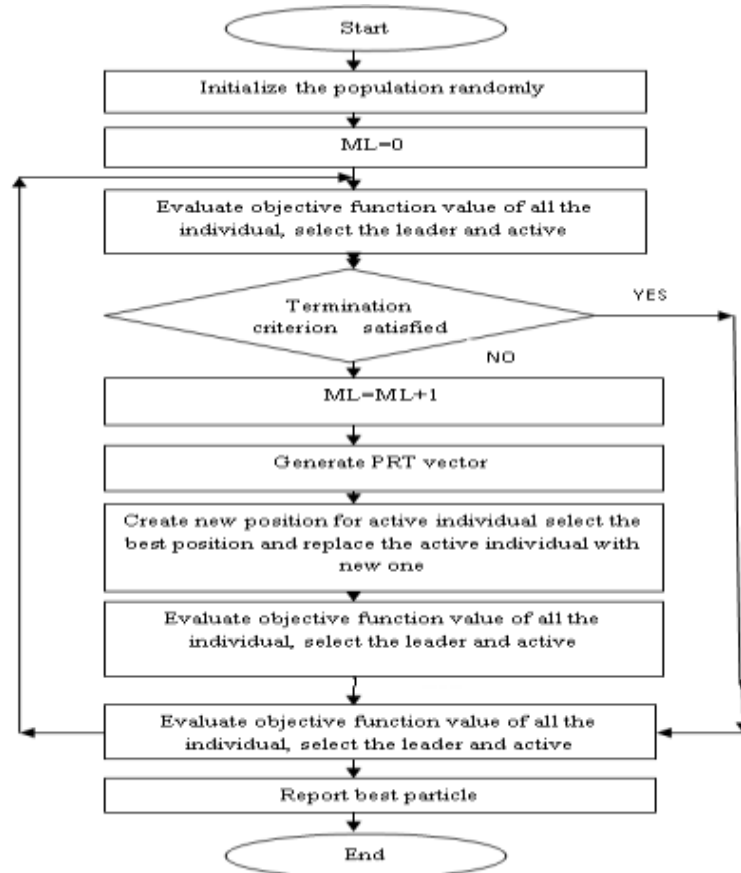


Figure 1: Flow chart of SOMA process

**The main parameters used in SOMA are described as follows [11]:**

**Pop Size:** defines the number of individuals in the population.

**Dimension:** represents the number of parameters of individual.

**Path Length (PL):** determines in what distance from the chef the actual individual will stop his movement.

**Step (t):** the granularity with which the search space is sampled.

**Perturbation (PRT):** determines whether an individual will travel directly towards the Chef, or not.

**Migrations (ML):** represents the number of migration loops or iterations.

In SOMA, the mutation is replaced by the perturbation and it depends on the perturbation vector. For each individual's parameter, the algorithm generates a random number ( $rnd_j$ ) from the interval [0,1] as the following expression.

*if  $rnd_j < PRT$ , then*

*$PRTVector_j = 1;$*

*else*

*$PRTVector_j = 0;$*

*end if*

Then, the following expression is used.

$$x_{i,j}^{MLnew} = x_{i,j,start}^{ML} + (x_{L,j}^{ML} - x_{i,j,start}^{ML}) \cdot PRTVector_j \quad (5.7)$$

where,  $t \in [0, \text{by step to, } PL]$ ,  $x_{i,j}^{MLnew}$  is the new positions of an individual,  $x_{i,j,start}^{ML}$  is the positions of active individual,  $x_{L,j}^{ML}$  is the positions of chef

**The computational steps of SOMA are given as follows [11]:**

**Step 1:** generate initial population;

**Step 2:** evaluate all individuals in the population;

**Step 3:** generate PRT vector for all individuals;

**Step 4:** sort all of them;

**Step 5:** select the best fitness individual as chef and worst as active;

**Step 6:** for active individual, new positions are created using equation 5.7. Then the best position is selected and replaces the active individual by the new one;

**Step 7:** if termination criterion is satisfied stop else go to step 2;

**Step 8:** report the best individual as the optimal solution.

### Simulation Results

This section illustrates the results obtained by using SOMA. To validate the performance of the SOMA, the PA and DPD was modeled using the memory polynomial model given by equation 4.23. All the characteristics of proposed algorithms for PA and DPD have been measured by sweeping value of memory length  $L$  between 1 to 5 and the nonlinearity order  $K$  between 2 to 7 in memory polynomial.

#### PA modeling

Figure 2 and figure 3 shows the AM-AM characteristics and the AM-PM characteristics respectively of actual PA and proposed SOMA PA model, the characteristics curve of proposed SOMA PA model is tried to follow the characteristics of actual PA. This shows the accuracy of the modeling in terms of AM-AM and AM-PM characteristics.

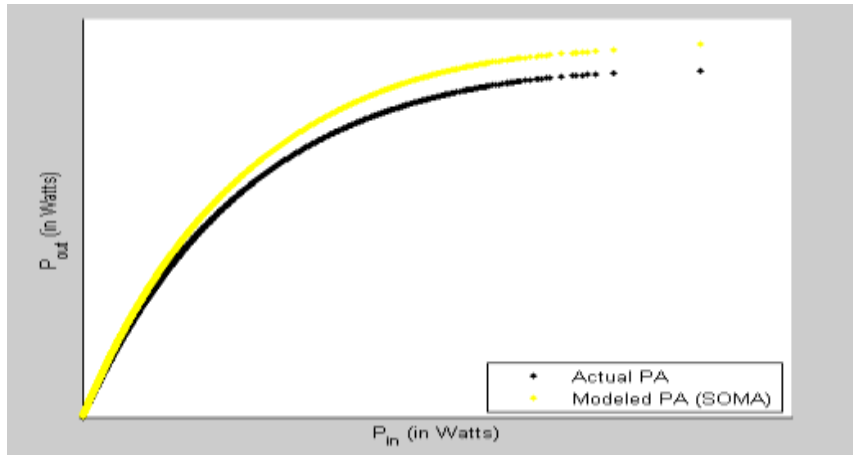


Figure 2: AM-AM characteristics for actual PA and proposed SOMA PA model

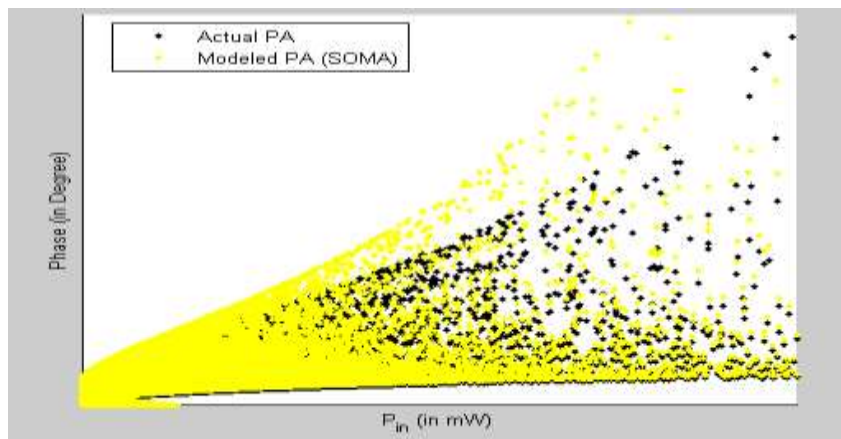


Figure 3: AM-PM characteristics for actual PA and proposed SOMA PA model

Figure 4 shows the power spectral density diagram of actual PA and proposed SOMA PA model, due to PA memory and non-linear effects, the spectrum of output signal has expanded.

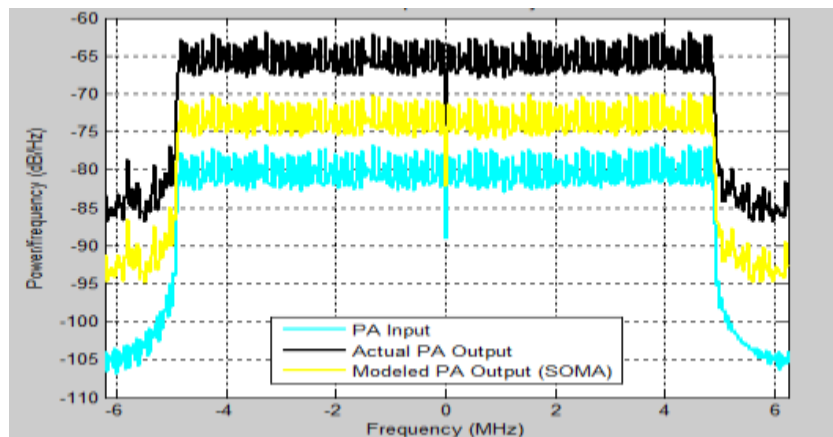


Figure 4: Power spectral density of actual and proposed SOMA PA model

The performance of proposed SOMA PA model based on ACLR (dB), EVM (dB), memory length and non linear order can be inferred from table 1. The ACLRs of actual PA are Lower Channel 2 (53.17), Lower Channel 1 (28.28), Upper Channel 1 (29.04) and Upper Channel 2 (54.01). The ACLRs of modeled PA using SOMA are Lower Channel 2 (58.02), Lower Channel 1 (30.26), Upper Channel 1 (31.18) and Upper Channel 2 (56.20).

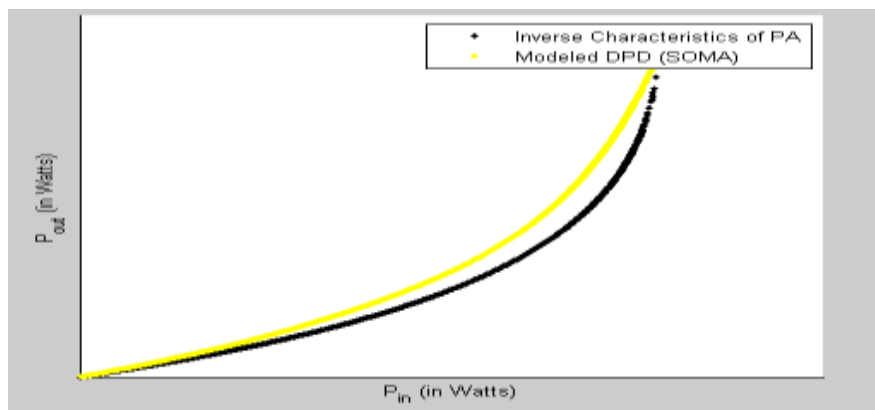
**Table 1:** Measurements of actual PA and proposed SOMA modelled PA

Type	Adjacent Channel Leakage Ratio (dB)				Error Vector Magnitude (dB)	Memory length	Non linear order
	Lower Channel 2	Lower Channel 1	Upper Channel 1	Upper Channel 2			
Actual PA	53.17	28.28	29.04	54.01	-24.28	-	-
SOMA PA	58.02	30.26	31.18	56.20	-26.23	3	7

From table 1, it has been concluded that all ACLRs of SOMA modelled PA have closer to the ACLRs of actual PA. For SOMA PA model, the values of memory length  $L$  and the nonlinearity order  $K$  are 3 and 7 respectively so, the no of coefficients are 21 in SOMA PA model. The EVM of actual PA and SOMA are -24.28 and -26.23 respectively, so the EVM of SOMA PA model (-24.26) is also closer to EVM of actual PA (-24.28).

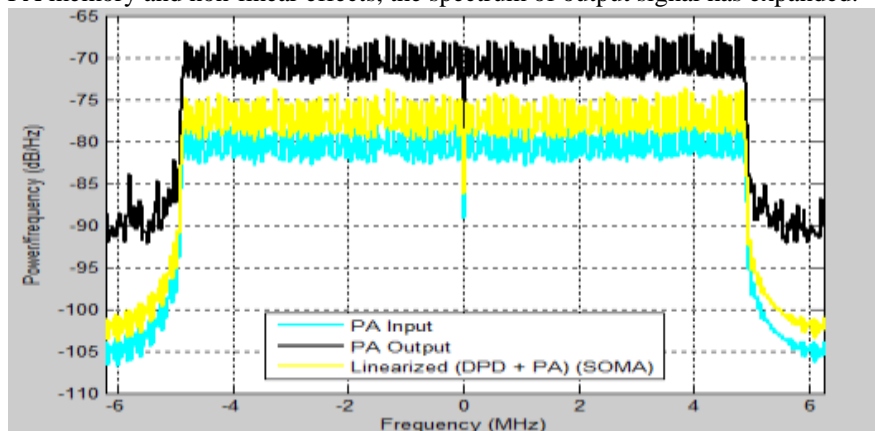
**DPD modeling**

Figure 5 shows the inverse AM-AM characteristics of actual PA and proposed SOMA DPD model. The characteristics curves of proposed SOMA modeled digital predistorter is tried to follow the inverse characteristics of actual PA, this shows the accuracy of the modeled SOMA digital predistorter.



**Figure 5:** AM-AM characteristics for proposed SOMA PA model and inverse AM-AM characteristics of actual PA

Figure 6 shows the power spectral density diagram of actual PA and digital predistorters of proposed SOMA, due to PA memory and non-linear effects, the spectrum of output signal has expanded.



**Figure 6:** Power spectral density of actual and modelled PA, modelled (DPD+PA) using proposed SOMA

The performance comparison of the proposed SOMA digital predistorters based on ACLR (dB), EVM (dB), memory length and non linear order can be inferred from table 2. The ACLRs of modeled digital predistorter using SOMA are Lower Channel 2 (78.06), Lower Channel 1 (52.19), Upper Channel 1 (53.05) and Upper Channel 2 (79.27). The ACLRs of actual PA are Lower Channel 2 (53.17), Lower Channel 1 (28.28), Upper Channel 1 (29.04) and Upper Channel 2 (54.01).

**Table 2:** DPD Performance Metrics using using proposed SOMA modeled DPD

Type	Adjacent Channel Leakage Ratio (dB)				Error Vector Magnitude (dB)	Memory length	Non linear order
	Lower Channel 2	Lower Channel 1	Upper Channel 1	Upper Channel 2			
Actual PA	53.17	28.28	29.04	54.01	-24.28	-	-
SOMA DPD	78.06	52.19	53.05	79.27	-28.25	4	7

From table 2, it has been concluded that the improvement in all ACLRs (Lower Channel 2 (24.89dB), Lower Channel 1 (23.91dB), Upper Channel 1 (24.01dB) and Upper Channel 2 (25.26dB)) of SOMA digital predistorter with respect to the actual PA all ACLRs. For SOMA DPD model, the values of memory length  $L$  and the nonlinearity order  $K$  are 4 and 7 respectively so, the no of coefficients are 28 in SOMA DPD model. The EVM of actual PA and SOMA PA model are -24.28 and -28.25 respectively, so the EVM (-3.97 dB) of SOMA DPD model has been improved with respect to the EVM of actual PA (24.28).

## II. CONCLUSION

The PA and DPD model measurement results based on a SOMA optimization algorithm shows the sovereignty of algorithm. The performance of proposed algorithm was tested on a wideband radio frequency power amplifier working for a 10 MHz WiMAX communication system. The PA results show the ACLR and EVM of modeled PA are very close to the actual PA, this shows the accuracy of proposed PA model. The DPD results concluded that approximately 24dB improvement in ACLR and EVM has also improved. The number of coefficients PA and DPD models are also less in proposed technique.

## REFERENCES

- [1]. Z. Shen, A. Papasakellariou, J. Montojo, and F. Xu (2012). Overview of 3GPP LTE advanced carrier aggregation for 4G wireless communications. *IEEE Communication. Mag.*, 50, 122-130.
- [2]. F. M. Ghannouchi and O. Hammi (2009). Behavioral modeling and predistortion. *IEEE Microwave Magazine*, 10, 52-64.
- [3]. W. Bösch and G. Gatti (1989). Measurement and simulation of memory effects in predistortion linearizers. *IEEE Trans. Microwave TheoryTech*, 37(12), 1885-1890.
- [4]. D. R. Morgan, Z. Ma, J. Kim, M. G. Zierdt, and J. Pastalan (2006). A generalized memory polynomial model for digital predistortion of RF power amplifiers. *IEEE Transactions in Signal Processing*, 54, 3852-3860.
- [5]. Rajbir Kaur (2016). Design and Implementation of An Efficient Digital Pre-Distorter for Wideband Wireless Transmitters, Doctoral Thesis. Faculty of Engineering and Technology, Punjabi University, Patiala.
- [6]. Zelinka, R. Senkerik, E. Navratil (2009). Investigation on evolutionary optimization of chaos control, *Chaos, Solitons & Fractals*, 40, 111-129.
- [7]. Kusum Deep and Dipti (2009). A self-organizing migrating genetic algorithm for constrained optimization. *Science direct, Applied Mathematics and Computation - Elsevier*, 198, 237-250.
- [8]. Leandro dos Santos Coelho (2009). Self-organizing migration algorithm applied to machining allocation of clutch assembly. *Science direct, Applied Mathematics and Computation - Elsevier*, 80, 427-435.
- [9]. L. Nolle, I. Zelinka, A.A. Hoptgood and A. Goodyear (2005). Comparison of a self-organizing migration algorithm with simulated annealing and differential evolution for automated waveform tuning. *Advances in Engineering Software*, 36, 645-653.
- [10]. M. Pant (2003). Genetic Algorithms for Global Optimization and Their Applications, Ph.D. Thesis. Department of Mathematics, IIT Roorkee, India.
- [11]. Donald Davendra and Ivan Zelinka (2016). Self-Organizing Migrating Algorithm Methodology and Implementation, *Studies in Computational Intelligence*. Springer International Publishing Switzerland 626, ISBN 978-3-319-28159-9.
- [12]. Amandeep Singh Sappal, Dr.Manjeet Singh Patterh and Dr. Sanjay Sharma (2011). Fast Complex Memory Polynomial Based Adaptive Digital Pre-Distorter. *International Journal of Electronics*, (Taylor and Francis), 98(7), 923-931.
- [13]. Yao X., Wang F., Padmanabhan K., and S. Salcedo-Sanz S. (2005). Hybrid evolutionary approaches to terminal assignment in communications networks. *Recent Advances in Memetic Algorithms, Studies in Fuzziness and Soft Computing*, 166(0), 129-159.
- [14]. NavidLashkarian, Jun Shi, and Marcellus Forbes (2014). A Direct Learning Adaptive Scheme for Power-Amplifier Linearization Based on Wirtinger Calculus. *IEEE Transactions on Circuits and Systems-I*, 61(12), 3496-3505.
- [15]. I. Zhang, Youjiang Liu, Jie Zhou, ShuboJin,Wenhua Chen and Silong Zhang (2015). A Band-Divided Memory Polynomial for Wideband Digital Predistortion With Limited Bandwidth Feedback. *IEEE Transactions on Circuits and Systems-II: 62(10)*, 922-925,
- [16]. Harjinder Singh and Amandeep Sappal (2015). Modeling and compensation of thermal effects in power amplifiers. *IEEE Conference in Computing and Communication (IEMCON)*, Vancouver, 1-7,
- [17]. SouhirLajnef, NouredineBoulejfen, Abubaker Abdelhafiz and Fadhel M. Ghannouchi (2016). Two-Dimensional Cartesian Memory Polynomial Model for Nonlinearity and I/Q Imperfection Compensation in Concurrent Dual-Band Transmitters. *IEEE Transactions on Circuits and Systems-II*, 63(1), 14-18.
- [18]. Nikolai Wolff, Wolfgang Heinrich and Olof Bengtsson (2016). A Novel Model for Digital Predistortion of Discrete Level Supply-Modulated RF Power Amplifiers. *IEEE Microwave and Wireless Components Letters*, 26(2), 146-148.
- [19]. Lie Zhang and Yan Feng (2016). An Improved Digital Predistortion in Wideband Wireless Transmitters Using an Under-Sampled Feedback Loop. *IEEE Communications Letters*, 20(5), 910-913.
- [20]. Li Gan (2009). Adaptive Digital Predistortion of Nonlinear Systems. Doctoral Thesis. Faculty of Electrical and Information Engineering, Graz University of Technology, Austria.